



Confederation of Indian Industry



CII APTDC Intellectual Property Capacity Building Workshop - (Series: IX)

Supported by



GLOBAL BEST PRACTICES IN PATENT INFORMATION ANALYSIS AND PATENT PROTECTION STRATEGIES

Saturday: 5 December 2009:1000-1700 Hrs: Hotel Taj Banjara: Hyderabad

Confederation of Indian Industry (CII) & Andhra Pradesh Technology Development and Promotion Centre (APTDC) in its IP Capacity Building Series IX is organizing a Workshop on Global Best Practices in Patent Information Analysis and Patent Protection Strategies on Saturday 5 December 2009 from 1000-1700 Hrs at Hotel Taj Banjara, Hyderabad

This Workshop is an exclusive and focused initiative to enhance the IP Skills of Industry Professionals through a Comprehensive Case Study Presentation & practical experience sharing by International experts.

CII APTDC has been focusing on Intellectual Property Capacity Building Initiatives across the Country covering key areas such as Freedom to Operate Tools, Patent Drafting, IP Valuation & Commercialization & IPR Enforcement The Present Workshop covers the sessions integrating IP Information for R&D Planning, showcasing state of the Art Indian Patent Information Tools, case studies on Patent Information tools for infringement analysis, Successful Patent Portfolio Strategies in ICT Sector , Patent Valuation and Commercialization (Life Sciences/Pharma/IT Sectors) This Workshop will be invaluable tool for all IP Professionals

Who Can Participate

All research, technical, development, and managerial personnel will benefit from this workshop.



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PROGRAM

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| 0930 – 1000 Hrs | Registration |
| 1000 – 1030 Hrs | Inaugural Session :Representative from Govt. of AP & CII, Andhra Pradesh |
| 1030 – 1130 Hrs | Session I: Integrating IP Information for R&D Planning |
| 1130 – 1200 Hrs | Session II: Showcasing State of the Art Indian Patent Information Tools |
| 1200 – 1245 Hrs | Session III :Case Studies on Patent Information tools for Infringement Analysis(Life Sciences/Pharma/IT Sectors) |
| 1245 – 1315 Hrs | Interactive Session |
| 1315 – 1400 Hrs | <i>Lunch</i> |
| 1400 – 1500 Hrs | Session IV :Case Studies on Successful Patent Portfolio Strategies in ICT Sector |
| 1500 – 1615 Hrs | Session V : Case Studies on Patent Valuation and Commercialization |
| 1615 – 1630 Hrs | Interactive Session |
| 1630 – 1700 Hrs | <i>Networking Hi Tea and Close</i> |

Faculty

Anne Kinsman



Anne Kinsman is a Partner at Borden Ladner Gervais LLP in Ottawa, Canada. Anne is a lawyer, and Registered Patent Agent in the United States and Canada. Anne obtained a B.Sc. in Engineering Physics (Electrical) from Queen's University, and an LL.B. from the University of Toronto. She is a Professional Engineer and is called to the Ontario Bar. Prior to entering the patent field, Anne worked as a design engineer in the piezoelectric transducer industry. She then worked as a Canadian Patent Examiner from 1988 – 1992. Anne now focuses her practice on drafting and prosecuting patent applications in the fields of electronics, wireless telecommunications, and semiconductor memory systems; managing patent portfolios for Canadian clients; conducting IP due diligence in relation to financing and acquisitions; and preparing infringement and validity opinions in support of patent litigation. Anne heads the high tech originating practice group in her firm.

Mukundan Chakrapani



Mukundan Chakrapani is a Associate at Borden Ladner Gervais LLP in Ottawa, Canada. He is a Registered Patent Agent in the United States and Canada. He graduated with a Bachelor and Masters degrees in Physics, with a concentration in Electronics, from the University of Madras. In addition, he was awarded a Diploma in Software Engineering, specializing in programming and database management. He subsequently received both a Masters and a Ph.D. in Physics from Florida State University in Tallahassee, Florida. Mukundan was a Visiting Fellow at the Steacie Institute for Molecular Sciences (SIMS) at the National Research Council Canada, where his research



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focused on understanding the interaction of amyloid-related peptides with model lipid membranes. Prior to his tenure at SIMS, he researched nano-structured polymer materials. Mukundan prepares patent applications in the fields of electronics, telecommunications, and medical devices; he assists in the management of patent portfolios for Canadian clients; and prepares patentability, infringement and validity opinions for his clients.



Vedant Pujari

Vedant Pujari is Head of Indian IP Information for Clairvortex Knowledge Processes Pvt. Ltd. A lawyer by training Vedant has several years of experience as an IP attorney specializing in trademarks. He heads the IP Information unit of Clairvortex and leads a team of IP analysts, information scientists, computer programmers and data processors. Vedant gives leadership to a one of its kind initiative of Clairvortex to build India's first comprehensive electronic IP database (CIPIS).



Hitesh Kumar Baliyan

Hitesh Baliyan is Head of Business Development for Clairvortex Knowledge Processes Pvt. Ltd. After acquiring a management degree in the year 2000, he joined the off-shoring industry and acquired diverse experience in sales and marketing of multiple businesses and verticals. He leads the business development functions at Clairvortex.



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REPLY FORM

Kamaraju Chitrapu

Executive Officer

Confederation of Indian Industry – APTDC

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I will / will not attend the above Session. (Please tick)

Our organisation will be represented by:

| <u>Sl No.</u> | <u>Name</u> | <u>Designation</u> | <u>Mobile</u> |
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